



## **Takumi Technology Announces Partnership with Toshiba**

### **Takumi Delivers Automated Full Chip Hot Spot Fixing for Sub-Wavelength Designs**

**Sunnyvale, Calif. – May 31, 2006** – Takumi Technology Corporation., the technology leader in system solutions for design-for-manufacturing (DFM), today announced a technology partnership with Toshiba Corporation, Semiconductor Company, a world leader in advanced semiconductor manufacturing. Under terms of the partnership, Toshiba will apply Takumi's automated layout modification methodology to its mask data preparation design flow in advanced IC manufacturing.

Hot spots are layout-related problems on a chip that lead to failure, impacting yield. At the sub-nanometer level, the number of hot spots on a chip grows dramatically with lower process nodes. Identifying and cleaning those hot spots is a time-consuming and expensive process.

Mr. S. Inoue, Group Manager of Lithography Process Development Group, Process and Manufacturing Engineering Center of Toshiba Semiconductor Company, remarked, "using Takumi's hot spot fixing methodology we were able to automate our hot spot cleaning process and improve our turn around time substantially, in spite of the complexity and large data files associated with sub-65nm SoC technology. We were able to reduce the number of hot spots from over 47,000 to just 40 in 12 hours. Furthermore, Takumi's vendor-agnostic approach enabled us to maximize the usage of our existing EDA software, thereby protecting our current investment."

#### **On-going Relationship**

The partnership enables on-going collaborative development, helping both companies. "For a young technology company such as Takumi to receive such a firm endorsement by a company of Toshiba Corporation's stature is a sign that we are providing tangible and measurable value to our customers, said Dr. Adriaan Ligtenberg, Takumi's President and CEO. "It also indicates that our DFM methodologies in mask data preparation, defect analysis and layout modification can truly close the growing gap between design and manufacturing for sub-wavelength processes."

#### **About Takumi Technology Corporation**

Founded in October 2003 and headquartered in Sunnyvale, California, Takumi Technology Corp. is the leading provider of design for manufacturing (DFM) systems solutions for sub-wavelength process nodes. Takumi Technology presently offers criticality aware solutions for backend tapeout flow, defect analysis and layout optimization. Takumi's criticality aware methodologies are completely vendor-agnostic and enable our customers to maximize their productivity and investment in their existing third party EDA tools. Takumi's technologies have been deployed with leading IDMs in 90nm, 65nm and 45nm technology nodes. Takumi Technology BV (The Netherlands) and Takumi Technology KK (Japan) are wholly owned subsidiaries of Takumi Technology Corp.

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